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APPLICANTS

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None *JM*** FOREIGN APPLICATIONS *****
None *JM*

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY OR	SHEETS DRAWING 9	TOTAL CLAIMS 25	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>Jeff Matatus</i> Examiner's Signature				
		<i>9/13</i> Initials		

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TITLE

Methods for minimizing the impedance discontinuity between a conductive trace and a component and structures formed thereby

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